

#5/IDS

Sheet 1 of 1

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		INFORMATION DISCLOSURE STATEMENT BY APPLICANT		APPLICANT	
		(Use several sheets if necessary)		ISHII, et al.	
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## U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
	AA					
	AB					
	AC					
	AD					
	AE					
	AF					
	AG					
	AH					
	AI					
	AJ					
	AK					
	AL					

## FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
at	AM	5-82787	1993	JP	HOLL 29/784		X
	AN						
	AO						
	AP						
	AQ						
	AR						
	AS						
	AT						

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

at	AU	IEICE Transactions, vol. E 74, pp.130-141, 1991
at	AV	IEEE International Solid State Circuit Conference, pp. 266- 267, 1996
at	AW	K. Yano et al., "A Room Temperature Single Electron
	AX	Memory Device Using Fine-Grain Polycrystalline Silicon",
	AY	IEDM 1993, pp. 541-544
	AZ	

Examiner <i>Wilson</i>	Date Considered 01/08/03
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